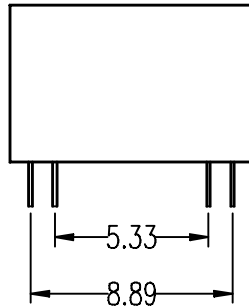
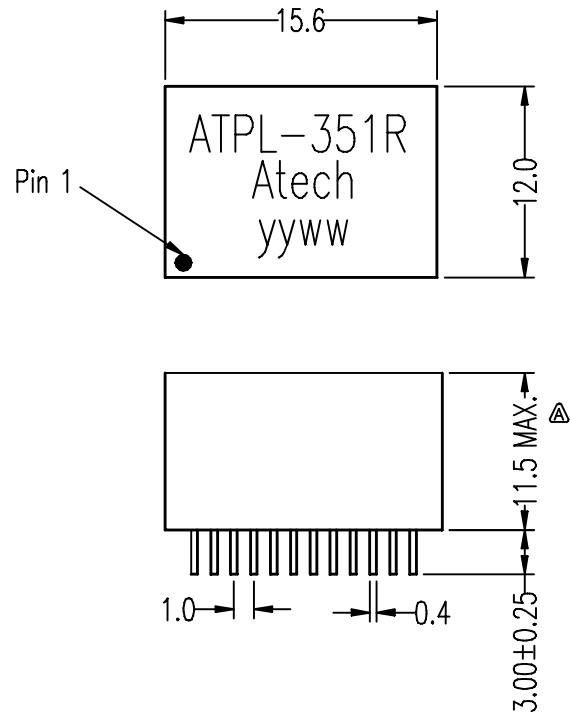
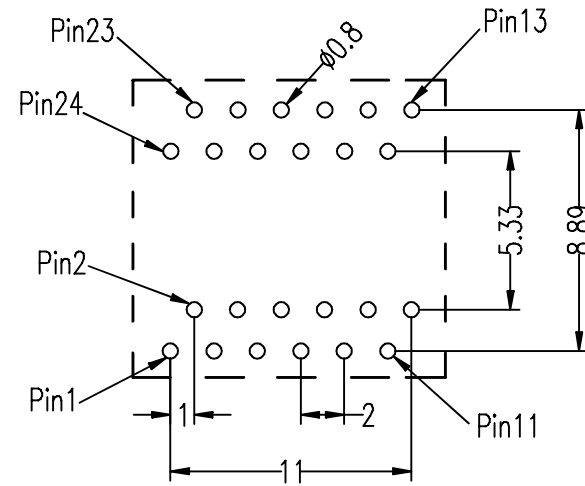


Mechanical Dimensions :



DIP Pad Dimensions : (TOP VIEW)

Hole Size



RECOMMENDED  
PAD LAYOUT

							TITLE 10/100/1000 BASE-TX TRANSFORMER	
							DWG. NO. ATPL-351R	
REVISIONS						UNITS:	SAFETY	SHEET 1 OF 3
NO:	DESCRIPTION	DATE	BY	CHK	APPD	DATE	P/N:	DRAW

OPERATING TEMPERATURE :  $-0^{\circ}\text{C}\sim+70^{\circ}\text{C}$   
 STORAGE TEMPERATURE :  $-20^{\circ}\text{C}\sim+85^{\circ}\text{C}$

ELECTRICAL SPECIFICATIONS @  $25^{\circ}\text{C}$

TURN RATIO : 1CT : 1CT(TX)/1CT : 1CT(RX)

OPEN CIRCUIT INDUCTANCE : 350uH MIN @100KHz,100mV,8 mA DC BIAS


INSERTION LOSS (TX/RX) :  $-1.0\text{dB}$  max (1~100MHz)

RETURN LOSS (TX/RX) :  $-18\text{dB}$  min(1~30MHz)  
 $-12\text{dB}$  min (60MHz)  
 $-10\text{dB}$  min(80MHz)

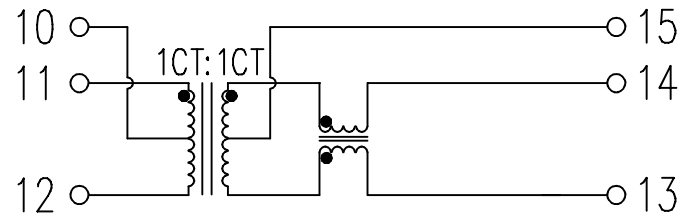
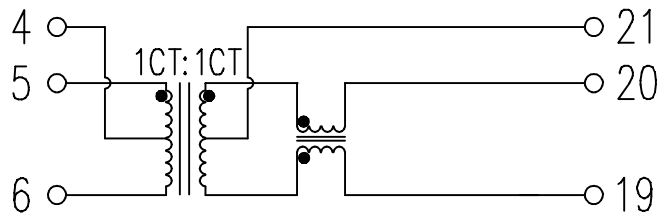
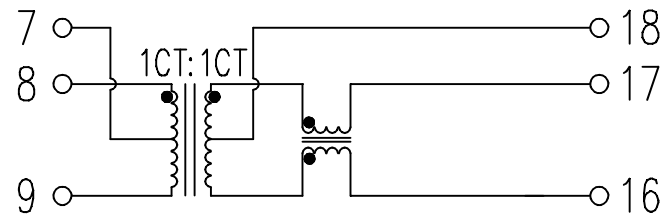
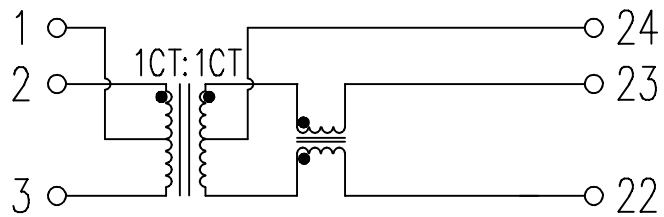
COMMON MODE REJECTION (TX/RX) :  $-40\text{dB}$  min(1~30MHz)  
 $-35\text{dB}$  min(60MHz)  
 $-30\text{dB}$  min (80~100MHz)


CROSS TALK :  $-40\text{dB}$  min(1~30MHz)  
 $-35\text{dB}$  min(60MHz)  
 $-30\text{dB}$  min(80~100MHz)

ISOLATION(PRIMARY/SECONDARY) : 1500 Vrms For 2sec,1mA Leakage Current

							TITLE 10/100/1000 BASE-TX TRANSFORMER	
							DWG. NO. ATPL-351R	
RELEASE						UNITS:	SAFETY	
NO:	DESCRIPTION	DATE	BY	CHK	APPD	DATE	P/N:	SHEET 2 OF 3
REVISIONS								DRAW

Circuit Schematic :



								TITLE 10/100/1000 BASE-TX TRANSFORMER	
REVISIONS						UNITS:		SAFETY	
NO:	DESCRIPTION	DATE	BY	CHK	APPD	DATE		P/N:	
								SHEET 3 OF 3 DRAW	